

Power Distribution Switch

Features

- 85mΩ High-Side MOSFET
- Available with 4 Versions of Current Limits with Foldback
- Operating Range: 2.7V to 5.5V
- 400μS Typical Rise Time
- Under Voltage Lockout
- 65μA Quiescent Supply Current
- 1μA Maximum Shutdown Supply Current
- Logic Level Enable Pin, Available with Active-High or Active-Low Version
- No Reverse Current when Power Off
- Deglitched Open-Drain Over-Current Flag Output (\overline{OC}) Available for 2 Kinds of Pin Out (G5250 E/F/G/H) (G5250 I/J/K/L)
- SOT-23-5 Package
- UL Approved #E232223
- Nemko IEC 60950-1 CB/CCA_scheme certification Report #67291
- CSA Approved #230321

Applications

- High-Side Power Protection Switch
- USB Power Management
- USB Host and Self-Powered Bubs
- USB Bus-Powered Hubs
- Hot Plug-In Power Supplies
- Battery-Charger Circuits

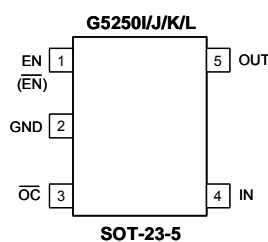
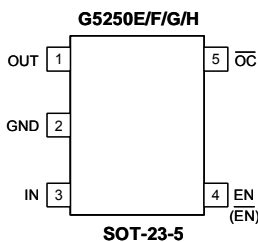
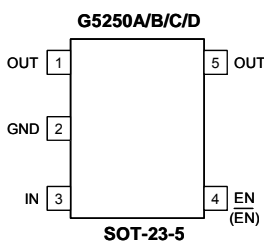
General Description

The G5250 is an integrated 85mΩ power switch for self-powered and bus-powered Universal Serial Bus (USB) applications.

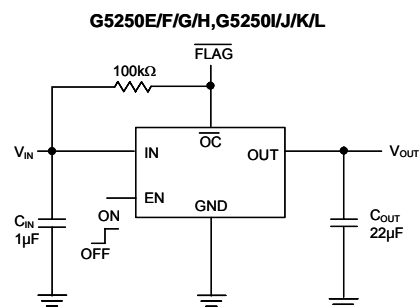
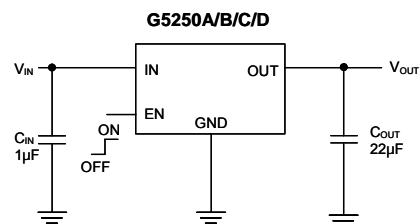
Several Protection features include current limiting with foldback, and thermal shutdown to prevent catastrophic switch failure caused by increasing power dissipation when continuous heavy loads or short circuit occurs. And a built-in charge pump is used to drive the N-channel MOSFET that is free of parasitic body diode to eliminate any reversed current flow across the switch when it is powered off.

G5250E/F/G/H and G5250I/J/K/L has open-drain \overline{OC} output reports over-current or over-temperature event and has typical 9ms deglitch timeout period.

Pin Configuration



Typical Application Circuit



UL Recognized Component

Ordering Information

ORDER NUMBER	MARKING	ENABLE	$\overline{\text{OC}}$ OUTPUT	CURRENT LIMIT	TEMP. RANGE	PACKAGE (Pb free)
G5250A1T1U	50A1x	Active High	No	2.5A	-40°C to +85°C	SOT-23-5
G5250A2T1U	50A2x	Active Low	No	2.5A	-40°C to +85°C	SOT-23-5
G5250B1T1U	50B1x	Active High	No	2.0A	-40°C to +85°C	SOT-23-5
G5250B2T1U	50B2x	Active Low	No	2.0A	-40°C to +85°C	SOT-23-5
G5250C1T1U	50C1x	Active High	No	1.5A	-40°C to +85°C	SOT-23-5
G5250C2T1U	50C2x	Active Low	No	1.5A	-40°C to +85°C	SOT-23-5
G5250D1T1U	50D1x	Active High	No	1.0A	-40°C to +85°C	SOT-23-5
G5250D2T1U	50D2x	Active Low	No	1.0A	-40°C to +85°C	SOT-23-5
G5250E1T1U	50E1x	Active High	Yes	2.5A	-40°C to +85°C	SOT-23-5
G5250E2T1U	50E2x	Active Low	Yes	2.5A	-40°C to +85°C	SOT-23-5
G5250F1T1U	50F1x	Active High	Yes	2.0A	-40°C to +85°C	SOT-23-5
G5250F2T1U	50F2x	Active Low	Yes	2.0A	-40°C to +85°C	SOT-23-5
G5250G1T1U	50G1x	Active High	Yes	1.5A	-40°C to +85°C	SOT-23-5
G5250G2T1U	50G2x	Active Low	Yes	1.5A	-40°C to +85°C	SOT-23-5
G5250H1T1U	50H1x	Active High	Yes	1.0A	-40°C to +85°C	SOT-23-5
G5250H2T1U	50H2x	Active Low	Yes	1.0A	-40°C to +85°C	SOT-23-5
G5250I1T1U	50I1x	Active High	Yes	2.5A	-40°C to +85°C	SOT-23-5
G5250I2T1U	50I2x	Active Low	Yes	2.5A	-40°C to +85°C	SOT-23-5
G5250J1T1U	50J1x	Active High	Yes	2.0A	-40°C to +85°C	SOT-23-5
G5250J2T1U	50J2x	Active Low	Yes	2.0A	-40°C to +85°C	SOT-23-5
G5250K1T1U	50K1x	Active High	Yes	1.5A	-40°C to +85°C	SOT-23-5
G5250K2T1U	50K2x	Active Low	Yes	1.5A	-40°C to +85°C	SOT-23-5
G5250L1T1U	50L1x	Active High	Yes	1.0A	-40°C to +85°C	SOT-23-5
G5250L2T1U	50L2x	Active Low	Yes	1.0A	-40°C to +85°C	SOT-23-5

Note: T1: SOT-23-5

U: Tape & Reel

Absolute Maximum Ratings

Supply Voltage (V_{IN})6V
 Output Voltage (V_{OUT})6V
 Output Current (I_{OUT})Internally Limited
 Enable Input (V_{EN})-0.3V to 6V
 Storage Temperature (T_S) -65°C to +150°C
 Reflow Temperature (soldering, 10sec) 260°C
 ESD protection2kV

Operating Ratings

Supply Voltage (V_{IN})+3V to +5.5V
 Operating Temperature (T_A)-40°C to +85°C

Electrical Characteristics

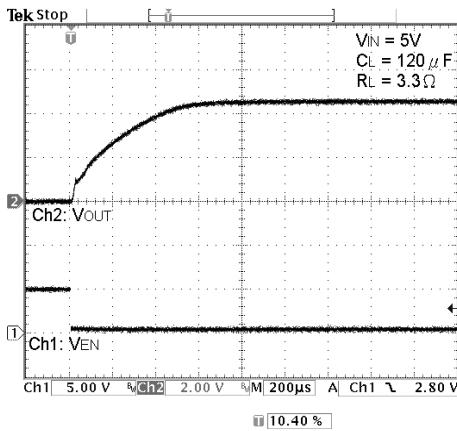
$V_{IN} = 5V$, $C_{IN} = 1\mu F$, $C_{OUT} = 1\mu F$, $R_L = 10\Omega$, $T_A = 25^\circ C$, unless otherwise noted.

PARAMETER	CONDITION	MIN	TYP	MAX	UNITS
Input Voltage Range		2.7	---	5.5	V
Output MOS $R_{DS(ON)}$	G5250A1/G5250A2/G5250E1/G5250E2/G5250I1/G5250I2, $I_{OUT} = 2A$	---	85	95	m Ω
	G5250B1/G5250B2/G5250F1/G5250F2/G5250J1/G5250J2, $I_{OUT} = 1.5A$				
	G5250C1/G5250C2/G5250G1/G5250G2/G5250K1/G5250K2, $I_{OUT} = 1A$				
	G5250D1/G5250D2/G5250H1/G5250H2/G5250L1/G5250L2, $I_{OUT} = 0.5A$				
Supply Current		---	65	---	μA
Output Turn-on Rising Time	$R_L = 10\Omega$, 90% Settling	---	400	---	μs
Current Limit Threshold	G5250A1/G5250A2/G5250E1/G5250E2/G5250I1/G5250I2, $V_{OUT} = 4V$, $2.7V < V_{IN} < 5.5V$	2	2.5	3.5	A
	G5250B1/G5250B2/G5250F1/G5250F2/G5250J1/G5250J2, $V_{OUT} = 4V$, $2.7V < V_{IN} < 5.5V$	1.5	2	2.8	
	G5250C1/G5250C2/G5250G1/G5250G2/G5250K1/G5250K2, $V_{OUT} = 4V$, $2.7V < V_{IN} < 5.5V$	1.1	1.5	2.1	
	G5250D1/G5250D2/G5250H1/G5250H2/G5250L1/G5250L2, $V_{OUT} = 4V$, $2.7V < V_{IN} < 5.5V$	0.7	1	1.4	
Short-circuit Current	G5250A1/G5250A2/G5250E1/G5250E2/G5250I1/G5250I2, $V_{OUT} = 0V$, $2.7V < V_{IN} < 5.5V$	0.2	1.6	2.3	A
	G5250B1/G5250B2/G5250F1/G5250F2/G5250J1/G5250J2, $V_{OUT} = 0V$, $2.7V < V_{IN} < 5.5V$	0.2	1.3	1.9	
	G5250C1/G5250C2/G5250G1/G5250G2/G5250K1/G5250K2, $V_{OUT} = 0V$, $2.7V < V_{IN} < 5.5V$	0.2	1	1.4	
	G5250D1/G5250D2/G5250H1/G5250H2/G5250L1/G5250L2, $V_{OUT} = 0V$, $2.7V < V_{IN} < 5.5V$	0.2	0.67	1	
EN Input Threshold		1.4	1.6	1.8	V
Shutdown Supply Current		---	0.1	1	μA
Output Leakage Current	EN="0", $V_{OUT} = 0V$	---	0.5	1	μA
V_{IN} Under Voltage Lockout		2.2	2.5	2.7	V
V_{IN} Under Voltage Hysteresis		---	200	---	mV
Thermal Limit		---	135	---	$^\circ C$
Thermal Limit Hysteresis		---	20	---	$^\circ C$
\overline{OC} Deglitch	G5250E/F/G/H, \overline{OC} assertion or deassertion	4	9	15	ms
\overline{OC} Output Low Voltage	G5250E/F/G/H, $I_{OC} = 5mA$	---	---	0.4	V
\overline{OC} Off-State Current	G5250E/F/G/H, $V_{OC} = 5V$	---	---	1	μA

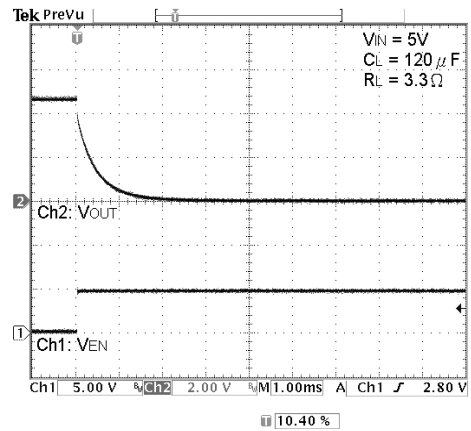
Typical Performance Characteristics

($V_{IN} = 5V$, G5250B2, $C_{IN} = 1\mu F$, $C_{OUT} = 1\mu F$, $V_{EN} = 0V$, $T_A = 25^\circ C$, unless otherwise noted.)

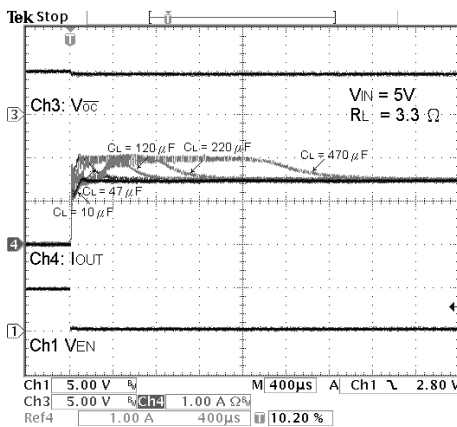
Turn on Delay Time and Rise Time



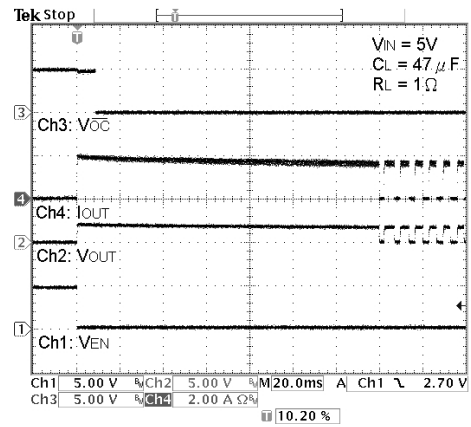
Turn off Delay Time and Fall Time



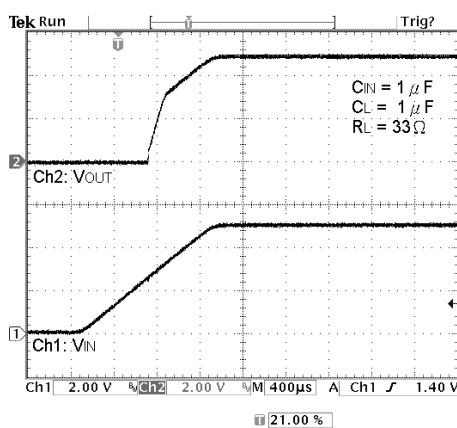
Inrush Current With Different Load Capacitance



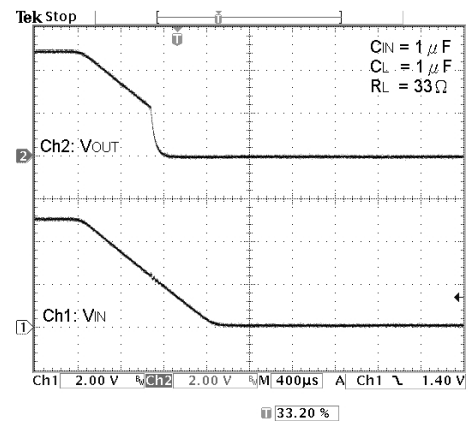
Thermal Shutdown Response



UVLO Protection at Rising

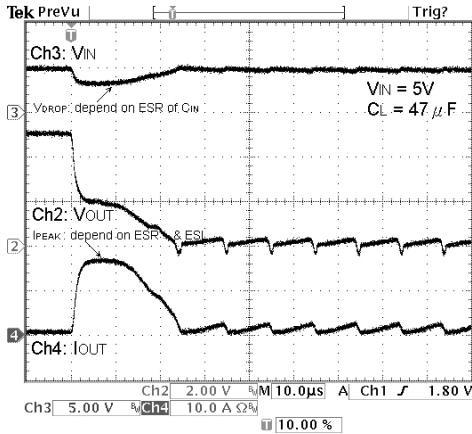


UVLO Protection at Falling

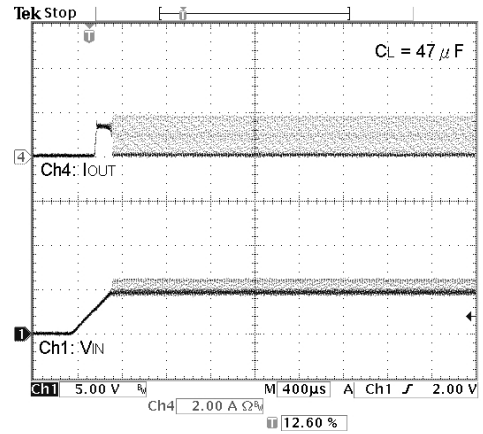


Typical Performance Characteristics (continued)

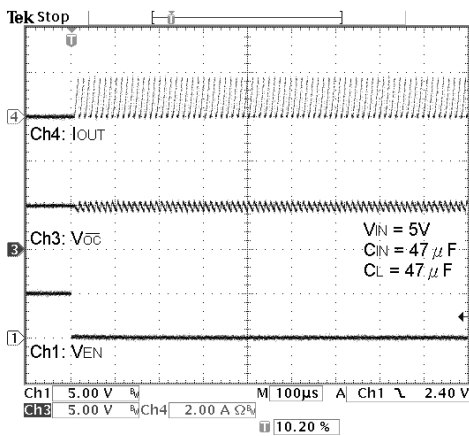
Inrush Short Circuit Response



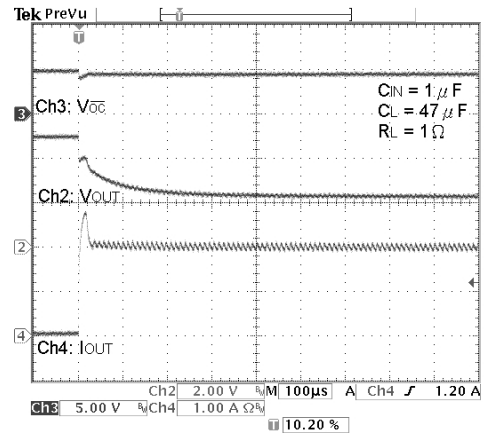
Short Circuit Response at Start up



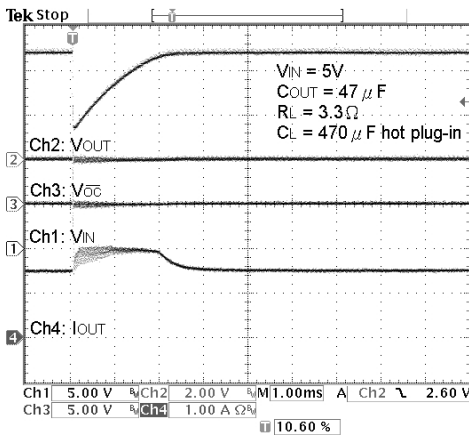
Short-Circuit Current, Device Enable into Short



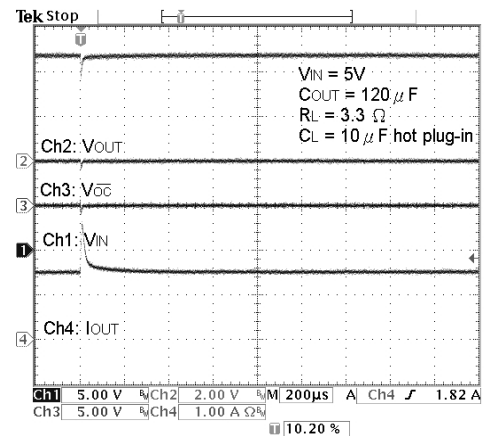
Resistance Load Inrush Response



Capacitance Load Inrush Response

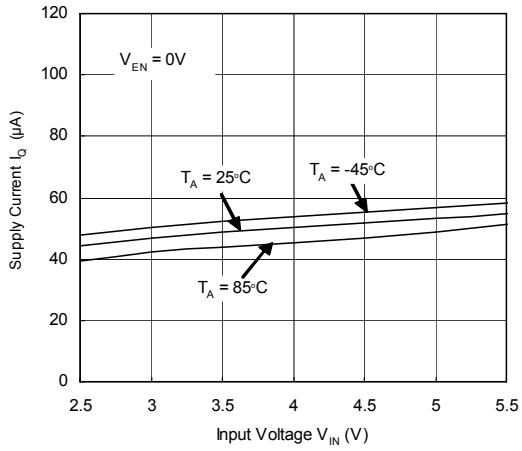


Capacitance Load Inrush Response

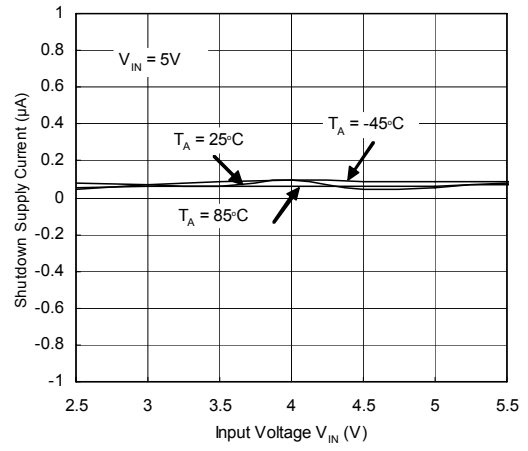


Typical Performance Characteristics (continued)

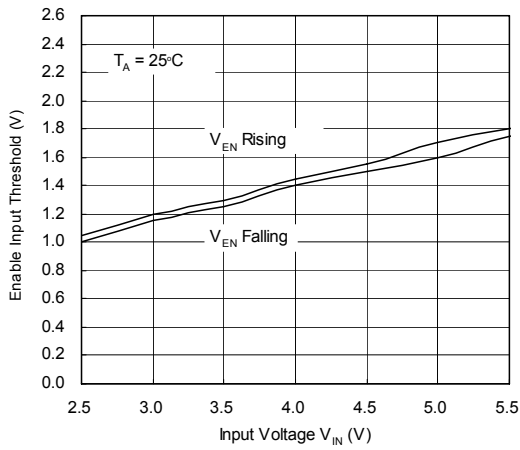
Supply Current vs. Input Voltage



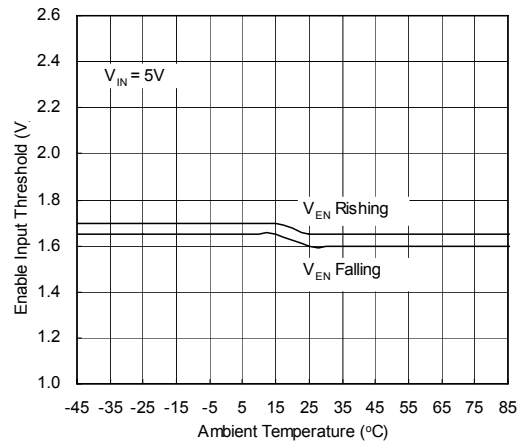
Shutdown Supply Current vs. Input Voltage



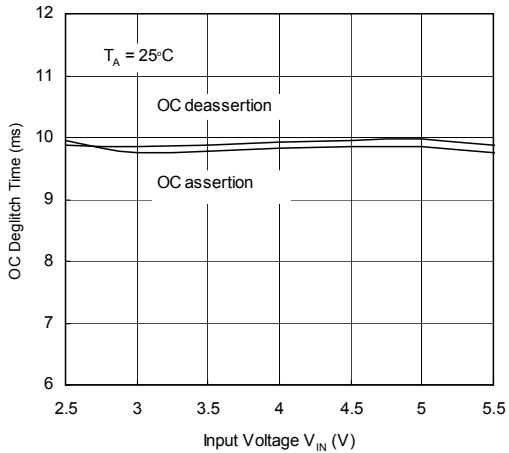
Enable Input Threshold vs. Input Voltage



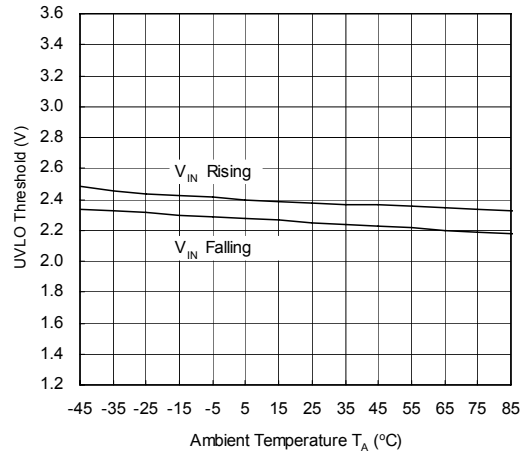
Enable Input Threshold vs. Temperature



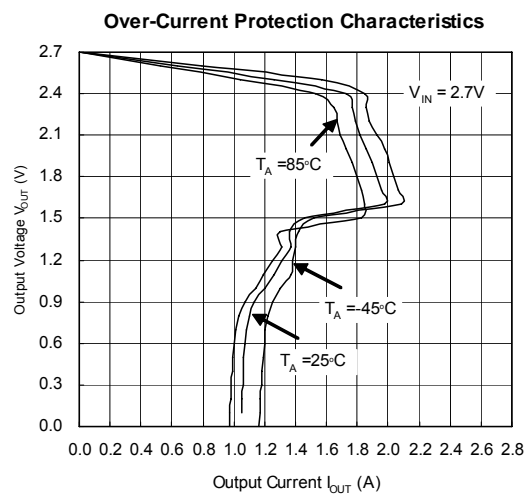
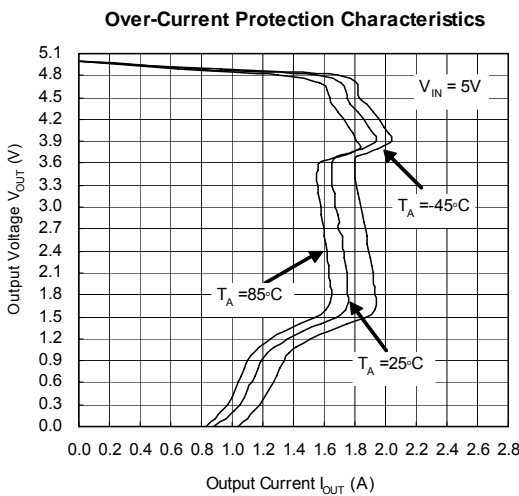
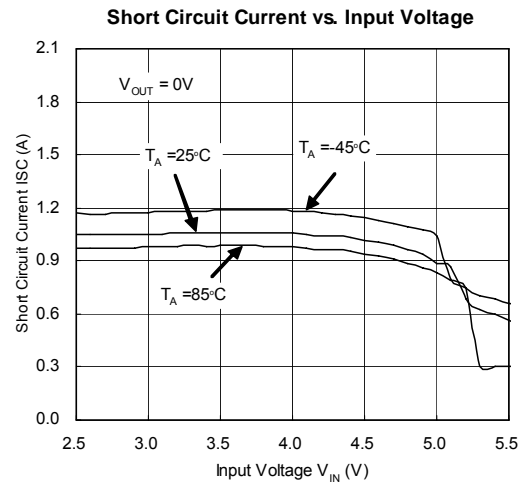
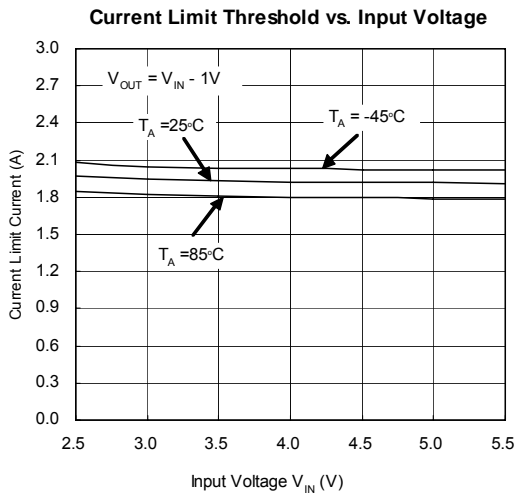
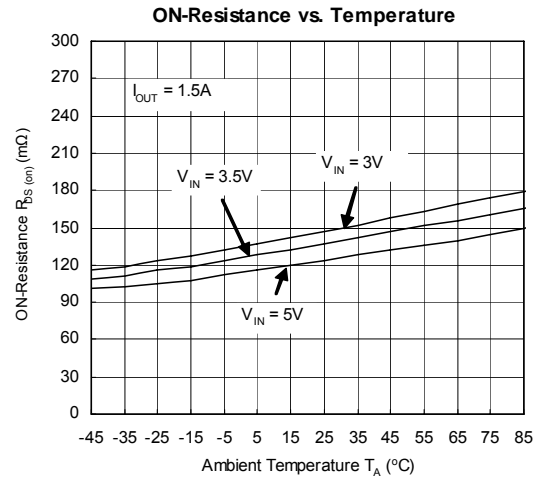
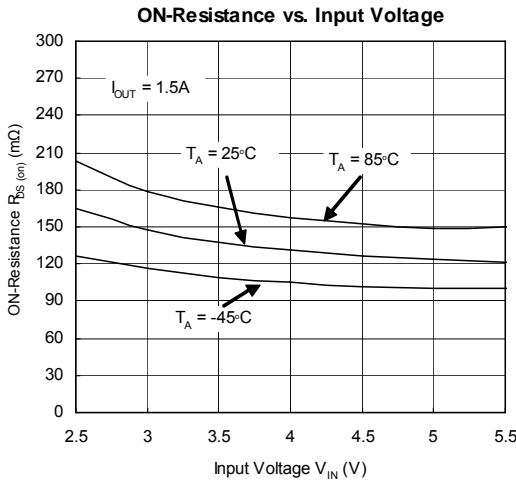
OC Deglitch Time vs. Input Voltage



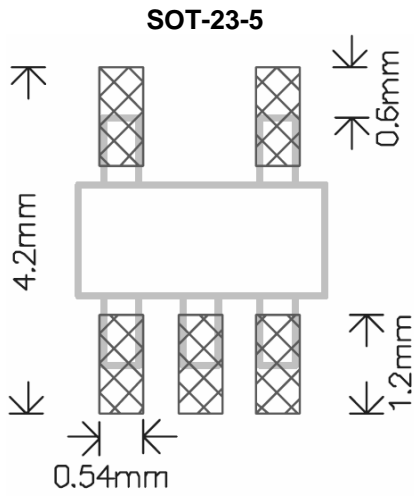
UVLO Threshold vs. Temperature



Typical Performance Characteristics (continued)



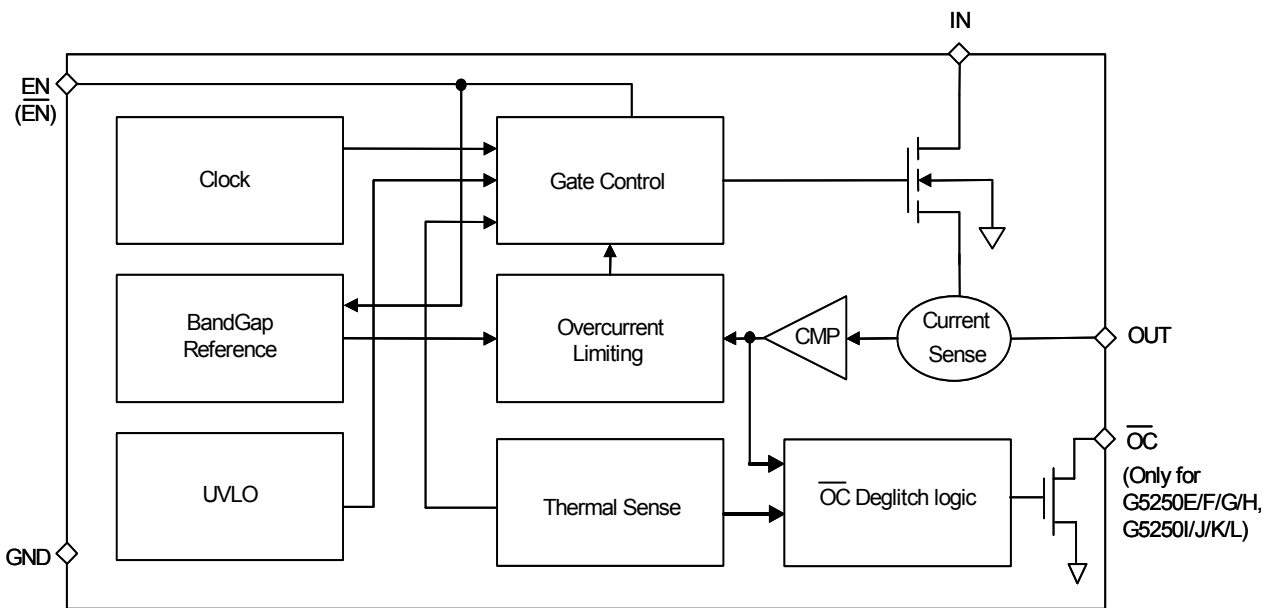
Recommended Minimum Footprint



Pin Description

PIN			NAME	PIN FUNCTION
G5250A/B/C/D	G5250E/F/G/H	G5250I/J/K/L		
1,5	1	5	OUT	Switch Output: Output MOSFET Source. Typically connect to switched side of load.
2	2	2	GND	Ground
3	3	4	IN	Input Supply: Output MOSFET Drain, which also supplies IC's internal circuitry. Connect to positive supply.
4	4	1	EN($\overline{\text{EN}}$)	Enable: Logic level enable input. Make sure EN pin never floating.
	5	3	$\overline{\text{OC}}$	Overcurrent open-drain $\overline{\text{OC}}$ output

Block Diagrams





Functional Description

Input and Output

IN (input) is the power supply connection to the logic circuitry and the drain of the output MOSFET. OUT (output) is the source of the output MOSFET. In a typical application, current flows through the switch from IN to OUT toward the load. Both OUT pins must be connected together to the load.

Thermal Shutdown

Thermal shutdown protects G5250 from excessive power dissipation. If the die temperature exceeds 135°C, the MOSFETS switch is shut off. 20°C of hysteresis prevents the switch from turning on until the die temperature drops to 115°C. Thermal shutdown circuit functions only when the switch is enabled.

Undervoltage Lockout

UVLO (undervoltage lockout) prevents the output MOSFET from turning on until IN (input voltage) exceeds 2.5V typically. After the switch turns on, if the voltage drops below 2.3V typically, UVLO shuts off the output MOSFET.

Current Limiting

The typical current limit value of G5250 is 2.5A, 2A, 1.5A, 1A for G5250A(E)(I) / G5250B(F)(J) / G5250C(G)(K) / G5250D(H)(L) respectively. There is foldback of current limit when $V_{OUT} < 1.5V$ (See Typical Performance Characteristics).

\overline{OC} Function (G5250E/F/G/H and G5250I/J/K/L)

The \overline{OC} open-drain output is asserted (active low) when an over current or overtemperature shutdown condition is encountered after a 9-ms deglitch timeout. The output remains asserted until the overcurrent or overtemperature condition is removed.

Applications Information

Supply Filtering

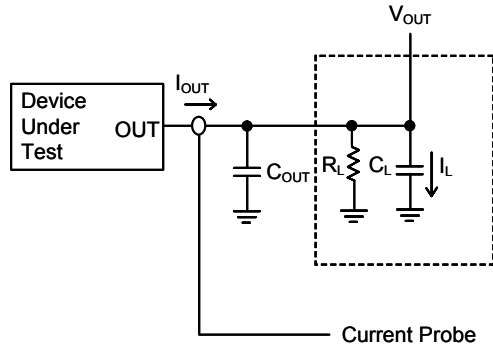
A 1 μ F bypass capacitor from IN to GND, located near the G5250, is strongly recommended to control supply transients. Without a bypass capacitor, an output short may cause sufficient ringing on the input (from supply lead inductance) to damage internal control circuitry.

Input transients must not exceed the absolute maximum supply voltage ($V_{IN\ max} = 6V$) even for a short duration.

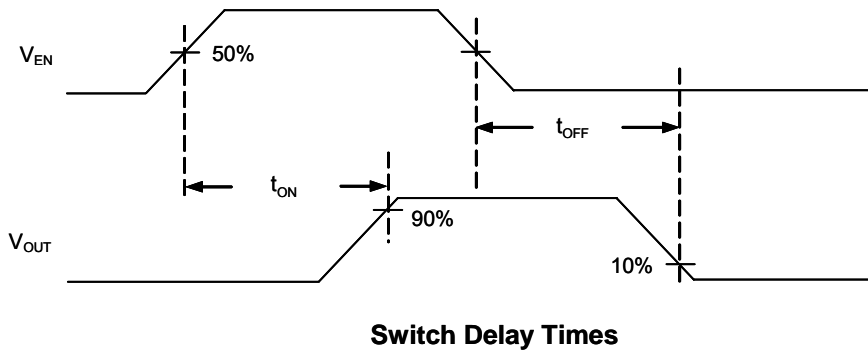
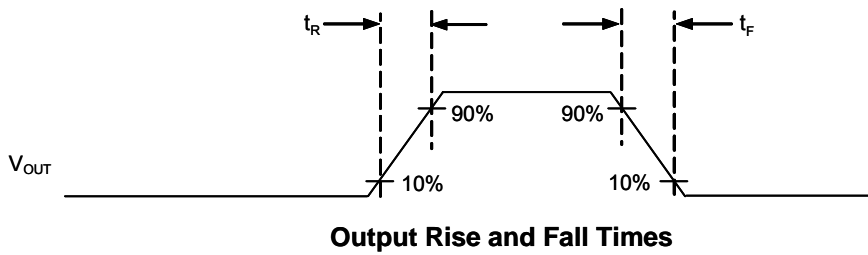
EN, the Enable Input

EN must be driven logic high or logic low for a clearly defined input. Floating the input may cause unpredictable operation. EN should not be allowed to go negative with respect to GND.

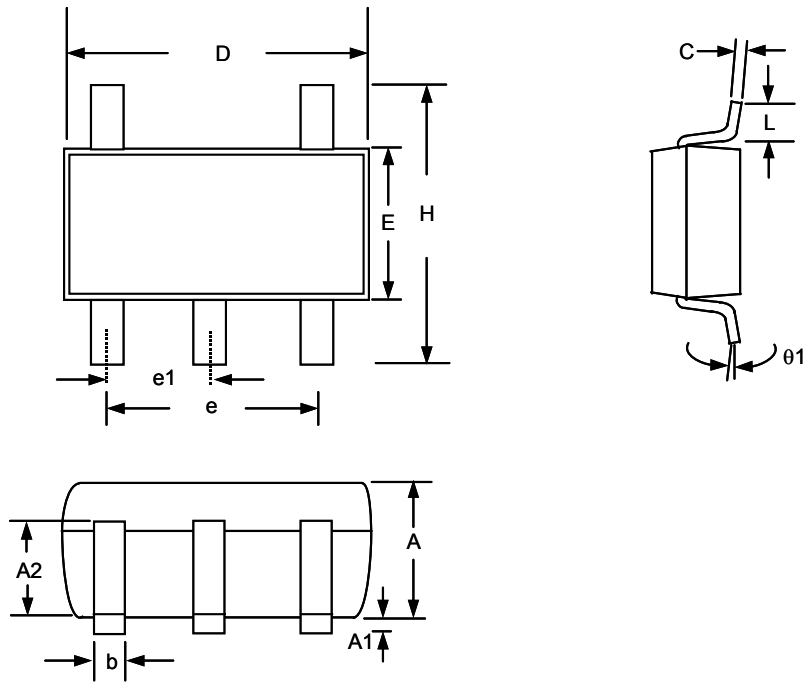
Test Circuit



Timing Diagrams



Package Information

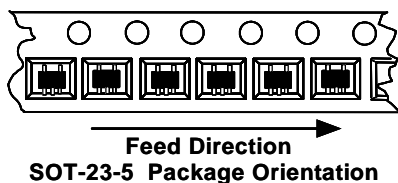


Note:

1. Package body sizes exclude mold flash protrusions or gate burrs
2. Tolerance ± 0.1000 mm (4mil) unless otherwise specified
3. Coplanarity: 0.1000mm
4. Dimension L is measured in gage plane

SYMBOLS	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX
A	1.00	1.10	1.30
A1	0.00	----	0.10
A2	0.70	0.80	0.90
b	0.35	0.40	0.50
C	0.10	0.15	0.25
D	2.70	2.90	3.10
E	1.40	1.60	1.80
e	----	1.90(TYP)	----
e1	----	0.95	----
H	2.60	2.80	3.00
L	0.37	----	----
$\theta 1$	1°	5°	9°

Taping Specification



PACKAGE	Q'TY/REEL
SOT-23-5	3,000 ea

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